Vishay Sfernice



Dual Value Chip Resistors, Center Tap



Actual Size

These tantalum chips combine excellent stability 0.07 % (2000 h, rated power at + 70 $^{\circ}$ C) with great power handling capacity. Two bonding pads per termination allow greater flexibility in hybrid layout design.

FEATURES

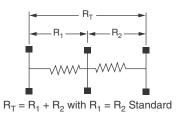
- Center tap feature
- Resistor material: self-passivating Tantalum Nitride
- Silicon substrate for good power dissipation
- · Low cost
- Wirebondable



TYPICAL PERFORMANCE

	ABS	TRACKING
TCR	100 ppm/°C	5 ppm/°C
	ABS	RATIO
TOL.	0.5 %	0.5 %

SCHEMATIC



STANDARD ELECTRICAL SPECIFICATIONS				
TEST		SPECIFICATIONS	CONDITIONS	
MATERIAL		TANTALUM NITRIDE		
Resistance range	e	50 Ω to 1 M Ω	for $R_T = R_1 + R_2$	
TCR:	Tracking	± 5 ppm/°C	- 55 °C to + 155 °C	
	Absolute	± 100 ppm/°C (± 50 ppm/°C on request)	- 55 °C to + 155 °C	
Ohmic value	Ratio	1/1 standard (unequal values: please consult)		
Tolerance:	Absolute	± 0.5 %, ± 1 %, ± 2 %		
	Matching	± 0.5 % standard		
Power dissipatio	n	250 mW at + 25 °C, 125 mW at + 70 °C, 50 mW at + 125 °C		
Stability		± 0.07 % typical, ± 0.1 maximum	2000 h at + 70 °C under Pn	
Working voltage		50 V _{DC} on R _T		
Operating tempe	rature range	- 55 °C to + 155 °C		
Storage tempera	ture range	- 55 °C to + 155 °C		
Noise		< - 35 dB typical	MIL-STD-202 Method 308	
Thermal EMF		0.01 μV/°C		
Shelf life stability	у	100 ppm	1 year at + 25 °C	

^{*} Please see document "Vishay Green and Halogen-Free Definitions (5-2008)" http://www.vishay.com/doc?99902

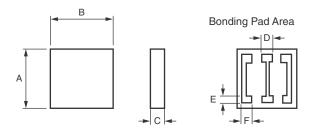




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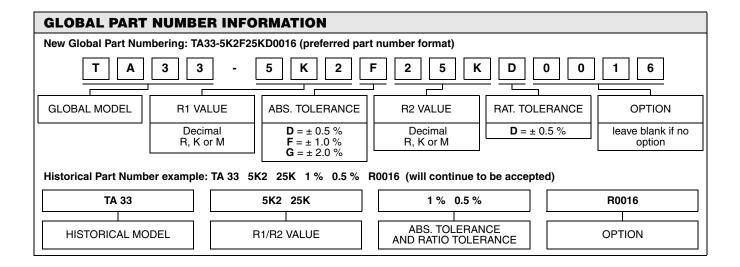
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DIMENSIONS



DIMENSION	INCHES	MILLIMETERS
Α	0.03 ± 0.004	0.76 ± 0.10
В	0.03 ± 0.004	0.76 ± 0.10
С	0.01 ± 0.015	0.25 ± 0.40
D	0.004	0.10
Е	0.006	0.15
F	0.006	0.15

MECHANICAL SPECIFICATIONS		
Resistive element	Tantalum Nitride	
Substrate material	Silicon	
Passivation	Autopassivation	
Bonding pads	Aluminum	





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